AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning at line 21 on page 9 of the specification with the following amended paragraph:

In a third stage of the initiating stages, decoding stage **314** (D stage), the PCU **226** decodes the fetched and aligned instruction out of the instruction packet. During the D stage **314** the PCU **226** sends information concerning the current load instruction to the ~~LSU~~ load/store unit (LSU) **218**. The four register file segments **224** each hold either floating-point data or integer data. The register file **216** is read in the decoding (D) stage **314**.